

J1130 U.S. PTO  
12/13/01

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**CERTIFICATE OF EXPRESS MAILING**

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on December 13, 2001 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number **EL580853675US**, addressed to the Commissioner for Patents, Washington, DC 20231.

*McLeod*  
Jeena McLeod

Attorney Docket No.: 01-721/LSI1P185

First Named Inventor: KAPRE

J1059 U.S. PTO  
10/02/04

12/13/01

**UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b))**

Commissioner for Patents  
Box Patent Application  
Washington, DC 20231.

To: This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors:  
**Ravindra M. Kapre, Tommy Hsiao, Yanhua Wang and Kyungjin Min**

For: **BURIED CHANNEL DEVICES AND A PROCESS FOR THEIR FABRICATION  
SIMULTANEOUSLY WITH SURFACE CHANNEL DEVICES TO PRODUCE TRANSISTORS  
AND CAPACITORS WITH MULTIPLE ELECTRICAL GATE OXIDES**

Assigned to: LSI LOGIC CORPORATION

Application Elements:

- 15 Pages of Specification, Claims and Abstract
- 9 Sheets of Formal Drawings
- 3 Pages Combined Declaration and Power of Attorney

Accompanying Application Parts:

- Do not publish this application. Nonpublication Request is attached.
- Assignment and Assignment Recordation Cover Sheet
- 37 CFR 3.73(b) Statement by Assignee
- Information Disclosure Statement with Form PTO-1449
  - Copies of IDS Citations
- Preliminary Amendment
- Return Receipt Postcard
- Other:

Fee Calculation (37 CFR § 1.16)

Applicant is entitled to Small Entity Status under 37 C.F.R. §1.27.

	(Col. 1) Total Claims		(Col. 2) Claims	(Col. 3) Present Extra	Rate	Additional Fee
TOTAL	21	MINUS	20	= 1	x 18 =	18.00
INDEP.	3	MINUS	3	= 0	x 84 =	0
[ ] First presentation of multiple dependent claim				\$280 =		
Basic Filing Fee under 37 C.F.R. §1.16(a)				\$740 =		740.00
				TOTAL		\$758.00
SMALL ENTITY 50% FILING FEE REDUCTION (if applicable)						

Check No. \_\_\_\_\_ in the amount of \$\_\_\_\_\_ is enclosed.

The Commissioner is authorized to charge any fees which may be required, or to credit any overpayment, to Deposit Account No. 12-2252 (Order No. 01-721/LSI1P185).

General Authorization for Petition for Extension of Time (37 CFR §1.136)

Applicants hereby make and generally authorize any Petitions for Extensions of Time as may be needed for any subsequent filings. The Commissioner is also authorized to charge any extension fees under 37 CFR §1.17 as may be needed to Deposit Account No. 12-2252 (Order No. 01-721/LSI1P185).

Please send correspondence to the following address:

**Customer Number 24319**  
LSI Logic Corporation  
1551 McCarthy Boulevard  
Milpitas, California  
Telephone (408) 433-6100



Date: December 13, 2001

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Steve D Beyer  
Registration No. 31,234

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: KAPRE et al.

Attorney Docket No.: 01-721/LSI1P185

Application No.: Unassigned

Examiner: Unassigned

Filed: Herewith

Group: Unassigned

Title: BURIED CHANNEL DEVICES AND A  
PROCESS FOR THEIR FABRICATION  
SIMULTANIOUSLY WITH SURFACE  
CHANNEL DEVICES TO PRODUCE  
TRANSISTORS AND CAPACITORS WITH  
MULTIPLE ELECTRICAL GATE OXIDES

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Jeena McLeod

**NONPUBLICATION REQUEST AND CERTIFICATION  
(35 U.S.C. 122(b)(2)(B)(i))**

Commissioner for Patents  
Box Patent Application  
Washington, D.C. 20231

Sir:

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen (18) months after filing. I hereby request that the attached application **NOT** be published under 35 U.S.C. 122(b).

Date: December 13, 2001  
Steve D. Beyer  
Registration No. 31,234